

CM300DX-12A
Dual IGBTMOD™ NX-Series Module
 300 Amperes/600 Volts

Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	CM300DX-12A	Units
Power Device Junction Temperature	T_j	-40 to 150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 to 125	$^\circ\text{C}$
Mounting Torque, M5 Mounting Screws	—	31	in-lb
Mounting Torque, M6 Main Terminal Screws	—	40	in-lb
Module Weight (Typical)	—	330	Grams
Baseplate Flatness, On Centerline X, Y (See Below)	—	$\pm 0 \sim +100$	μm
Isolation Voltage, AC 1 minute, 60Hz Sinusoidal	V_{ISO}	2500	Volts

Inverter Sector

Collector-Emitter Voltage (G-E Short)	V_{CES}	600	Volts
Gate-Emitter Voltage (C-E Short)	V_{GES}	± 20	Volts
Collector Current ($T_C = 56^\circ\text{C}$)*1	I_C	300	Amperes
Peak Collector Current (Pulse)*3	I_{CM}	600	Amperes
Emitter Current ($T_C = 25^\circ\text{C}$)*1*4	I_E^{*2}	300	Amperes
Peak Emitter Current (Pulse)*3	I_{EM}^{*2}	600	Amperes
Maximum Collector Dissipation ($T_C = 25^\circ\text{C}$)*1*4	P_C	960	Watts

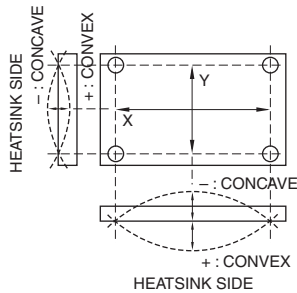
*1 Case temperature (T_C) and heatsink temperature (T_f) are defined on the surface of the baseplate and heatsink at just under the chip.

*2 I_E , I_{EM} , V_{EC} , t_{rr} and Q_{rr} represent ratings and characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

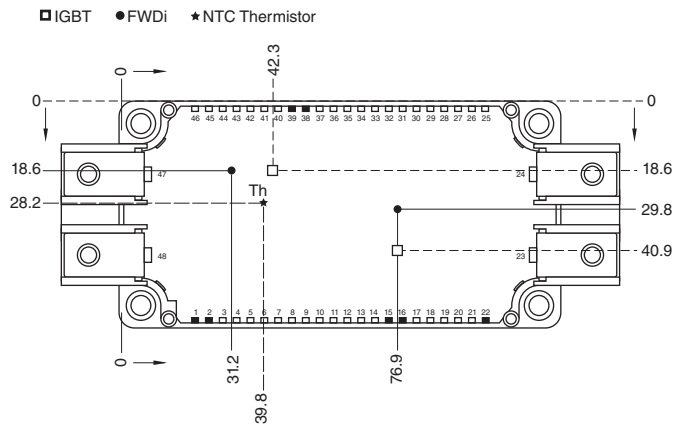
*3 Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed $T_{j(max)}$ rating.

*4 Junction temperature (T_j) should not increase beyond $T_{j(max)}$ rating.

BASEPLATE FLATNESS MEASUREMENT POINT



CHIP LOCATION (TOP VIEW)



Dimensions in mm (Tolerance: $\pm 1\text{mm}$)

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Electrical and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Inverter Sector

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector Cutoff Current	I_{CES}	$V_{CE} = V_{CES}, V_{GE} = 0V$	—	—	1.0	mA
Gate-Emitter Threshold Voltage	$V_{GE(th)}$	$I_C = 30mA, V_{CE} = 10V$	5	6	7	Volts
Gate Leakage Current	I_{GES}	$V_{GE} = V_{GES}, V_{CE} = 0V$	—	—	0.5	μA
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 300A, V_{GE} = 15V, T_j = 25^\circ\text{C}^5$	—	1.7	2.1	Volts
		$I_C = 300A, V_{GE} = 15V, T_j = 125^\circ\text{C}^5$	—	1.9	—	Volts
		$I_C = 300A, V_{GE} = 15V, \text{Chip}$	—	1.6	—	Volts
Input Capacitance	C_{ies}		—	—	34.0	nF
Output Capacitance	C_{oes}	$V_{CE} = 10V, V_{GE} = 0V$	—	—	4.0	nF
Reverse Transfer Capacitance	C_{res}		—	—	1.2	nF
Total Gate Charge	Q_G	$V_{CC} = 300V, I_C = 300A, V_{GE} = 15V$	—	800	—	nC
Inductive Load	Turn-on Delay Time	$V_{CC} = 300V, I_C = 300A,$ $V_{GE} = \pm 15V,$ $R_G = 5.1\Omega, I_E = 300A,$	—	—	200	ns
	Turn-on Rise Time		t_r	—	—	150
Switch Time	Turn-off Delay Time	$R_G = 5.1\Omega, I_E = 300A,$	—	—	350	ns
	Turn-off Fall Time		t_f	—	—	600
Reverse Recovery Time	t_{rr}^{*2}	Inductive Load Switching Operation	—	—	200	ns
Reverse Recovery Charge	Q_{rr}^{*2}		—	9	—	μC
Emitter-Collector Voltage	V_{EC}^{*2}	$I_E = 300A, V_{GE} = 0V, T_j = 25^\circ\text{C}^5$	—	2.0	2.8	Volts
		$I_E = 300A, V_{GE} = 0V, T_j = 125^\circ\text{C}^5$	—	1.95	—	Volts
		$I_E = 300A, V_{GE} = 0V, \text{Chip}$	—	1.9	—	Volts

Thermal and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Module Lead Resistance	R_{lead}	Main Terminals-Chip (Per Switch)	—	1.1	—	m Ω
Thermal Resistance, Junction to Case**	$R_{th(j-c)Q}$	Per IGBT*1	—	—	0.13	$^\circ\text{C}/W$
Thermal Resistance, Junction to Case**	$R_{th(j-c)D}$	Per FWDi*1	—	—	0.22	$^\circ\text{C}/W$
Contact Thermal Resistance**	$R_{th(c-f)}$	Case to Heatsink (Per 1 Module) Thermal Grease Applied*1*7	—	0.015	—	$^\circ\text{C}/W$
Internal Gate Resistance	R_{Gint}	$T_C = 25^\circ\text{C}$	—	0	—	Ω
External Gate Resistance	R_G		2.0	—	21	Ω

NTC Thermistor Sector, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Zero Power Resistance	R	$T_C = 25^\circ\text{C}^1$	4.85	5.00	5.15	k Ω
Deviation of Resistance	$\Delta R/R$	$T_C = 100^\circ\text{C}, R_{100} = 493\Omega^1$	-7.3	—	+7.8	%
B Constant	$B_{(25/50)}$	$B = (\ln R_1 - \ln R_2) / (1/T_1 - 1/T_2)^6$	—	3375	—	K
Power Dissipation	P_{25}	$T_C = 25^\circ\text{C}^1$	—	—	10	mW

**Thermal resistance values are per 1 element.

*1 Case temperature (T_C) and heatsink temperature (T_f) are defined on the surface of the baseplate and heatsink at just under the chip.

*2 $I_E, I_{EM}, V_{EC}, t_{rr}$ and Q_{rr} represent ratings and characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

*5 Pulse width and repetition rate should be such as to cause negligible temperature rise.

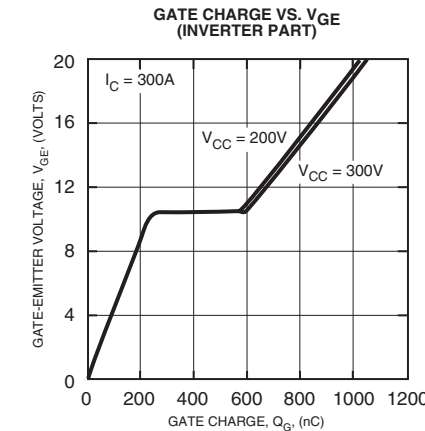
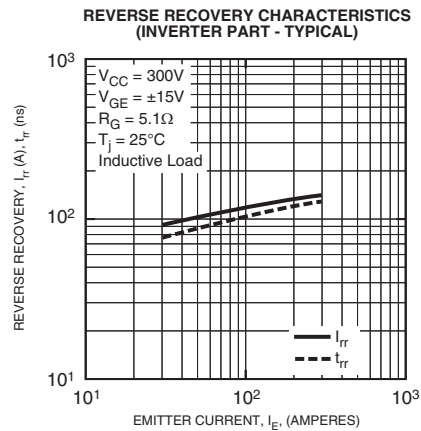
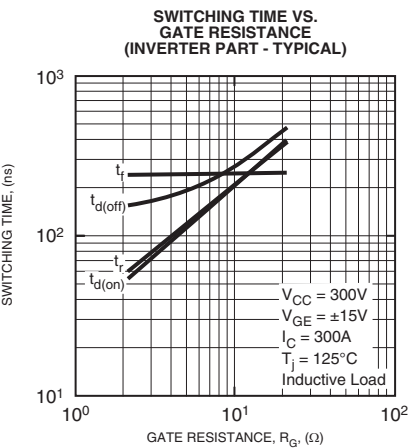
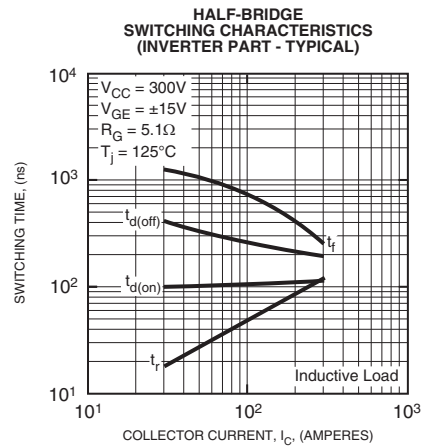
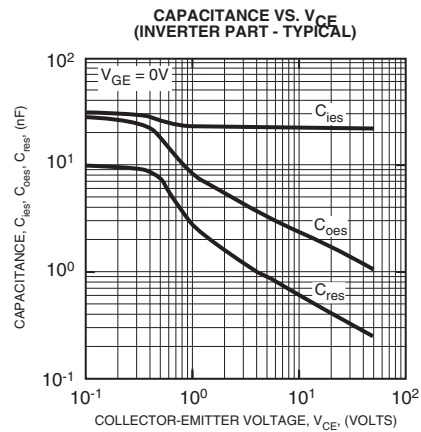
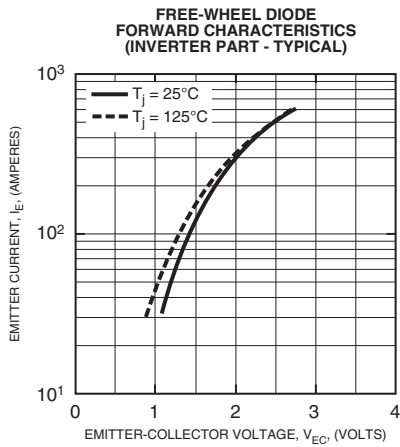
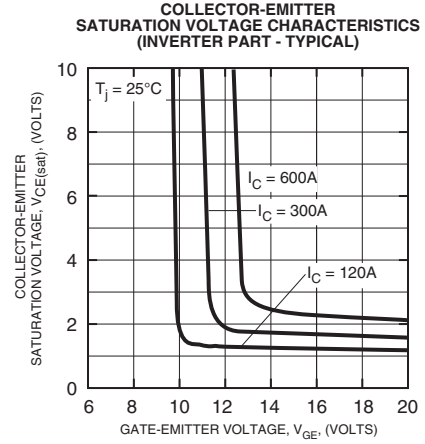
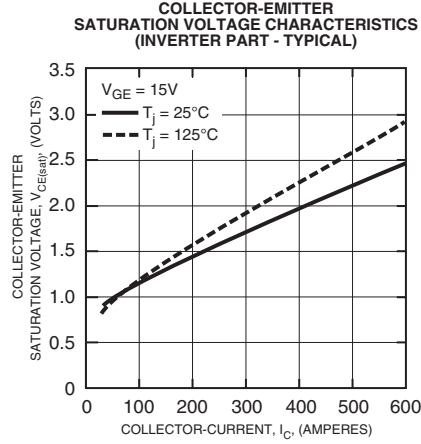
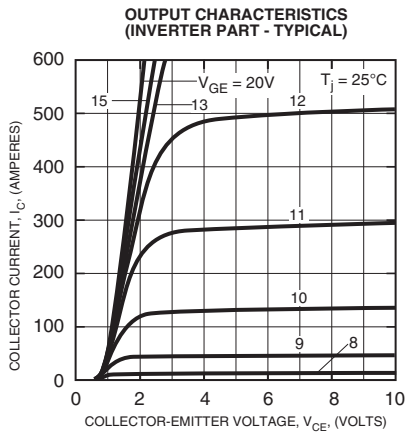
*6 R_1 : Resistance at Absolute Temperature $T_1(K)$, R_2 : Resistance at Absolute Temperature $T_2(K)$, $T(K) = T(^\circ\text{C}) + 273.15$

*7 Typical value is measured by using thermally conductive grease of $\lambda = 0.9 \text{ [W/(m} \cdot \text{K)]}$.



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